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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentees : Travis Swanson
Assignee : Micron Technology, Inc.
Patent No. : 7,058,778 B2
Issued : June 6, 2006
Application No. : 09/943,320
Confirmation No. : 8733
Filed : August 30, 2001
For : MEMORY CONTROLLERS HAVING PINS WITH
SELECTABLE FUNCTIONALITY
Art Unit : 2189
Examiner : Woo H. Choi

New York, New York 10036
June 12, 2008

ATTN: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Certificate
JUN 19 2008
of Correction

REQUEST UNDER 37 C.F.R. § 1.322
FOR CERTIFICATE OF CORRECTION

Sir:

Enclosed are three (3) sheets of Form PTO/SB/44 listing printing errors that have been found in the above-identified patent. The errors are clerical and self-explanatory and their correction does not constitute new matter and requires no reexamination.

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The errors are believed to be the responsibility of the PTO which published the original claims rather than the amended claims, therefore patentees believe no fee is due. However, if for any reason a fee is required in connection with this Request, please charge that fee, or credit any overpayment to Deposit Account No. 06-1075. A duplicate copy of this Request is enclosed herewith.

Accordingly, patentees and assignee respectfully request that the Patent and Trademark Office issue a Certificate of Correction Under 37 C.F.R. § 1.322.

Respectfully submitted,



Michael J. Chasan
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Agent for Patentees and Assignee

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CERTIFICATE OF CORRECTION**

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APPLICATION NO.: 09/943,320

ISSUE DATE: : June 6, 2006

INVENTOR(S) : Travis Swanson

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- Claim 1, line 17, before "said method" insert -- wherein at least one of said pins has selectable functionality--;
- line 21, after "pins" insert -- having said selectable functionality --;
- Claim 4, line 28, change "2" to --1-- ;
- Claim 8, line 41, replace "control" with --chip select--;
- line 43, replace "control" with --chip select--;
- Claim 12, line 53, replace "at least one" with --an--;
- line 56, replace "a control signal" with --a clock signal --;
- line 60, insert before "signal" --control--;
- line 61, replace "control" with --clock--;
- Claim 16, line 4, replace "at least one" with --an--;
- lines 6-8, replace "selectable functionality in accordance with a type of memory, said functionality including control chip select, and clock function" with -- a function selected in accordance with a type of memory, said circuitry operative to select one of at least a chip select function and a clock function --;
- Claim 17, lines 9-11, replace "said functionality of said output pin is one of a chip select signal and a clock signal" with --selection of the chip select function provides a chip select signal to said output pin; and wherein selection of the clock function provides a clock signal to said output pin--;
- Claim 18, lines 18-19, replace "functionality including control, chip select, and clock functions" with --memory controller operative to select a chip select function and a clock function for each of said at least one of said pins having the selectable functionality --;

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- Claim 19, lines 20-21, replace "said selectable functionality comprises" with -- selection of the clock function provides --;
- Claim 20, lines 22-23, replace "said selectable functionality comprises" with -- selection of the chip select function provides --;
- Claim 21, lines 32-35, replace "a subset of said pins has selectable functionality, said functionality based on said type of said memory modules, said functionality including control, chip select, and clock functions" with -- each one of a subset of said pins has selectable functionality, said selectable functionality based on said type of said memory modules, said selectable functionality including a chip select function and a clock function --;
- Claim 24, lines 47-48, replace "said type of memory, said functionality including control, chip select, and clock functions" with -- said type of memory, each of said at least one pin configurable by said means for configuring having selectable functionality including a chip select function and a clock function --;
- Claim 25, line 54, replace "control" with -- chip select --;
- Claim 26, line 6, replace "at least one" with -- an --;
- line 9, insert --for selecting-- after "means";
- line 13, insert --one of-- after "selecting"; delete "control"
- line 14, insert --for selecting and said clock signal means-- after "means";
- line 16, replace "at least one" with --an--;
- line 20, replace "control" with --chip select--;
- line 23, replace "control" with --chip select--;
- Claim 28, line 26, replace "at least one" with --an-- ;

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Claim 28 (continued)

lines 28-30, replace "said functionality including control, chip select, and clock functions" with --
said selectable functionality including a chip select function and a clock function --;

line 35, replace "at least some" with -- one --;

line 37, insert -- selectable -- after "said";

lines 38-39, replace "said functionality including control, chip select, and clock functions" with --
said selectable functionality including a chip select function and a clock function --;

Claim 30, line 48, insert -- each one of -- before "a subset of...";

lines 49-51, replace "said functionality based on said type of said memory modules and
including control, chip select, and clock functions" with -- said selectable functionality
based on said type of said memory modules and including a chip select function and a
clock function --

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